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Subclass
Class
ISSUE CLASSIFICATION

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U.S. UTILITY Patent Application

RS	O.I.P.E.	PATENT DATE
SCANNED	4Sg	O.A. 0/1

APPLICATION NO. 09/497916	CONT/PRIOR F	CLASS 257	SUBCLASS 324	ART UNIT 2811	EXAMINER Nada Lote
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APPLICANTS
Antonino Torres
Sergio Spampinato

TITLE
Integrated circuit including a vertical power component, a control circuitry thereof, and a protection structure against polarity inversion of the substrate potential

PTO-2040
12/99

ISSUING CLASSIFICATION					
ORIGINAL		CROSS REFERENCE(S)			
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)		
INTERNATIONAL CLASSIFICATION					
□ Continued on Issue Slip Inside File Jacket					

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	(Assistant Examiner) _____ (Date) _____			NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____	(Primary Examiner) _____ (Date) _____			ISSUE FEE	
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